

Title: JP1140781A2: LIGHT AMPLIFIER

Derwent Title:

Photo-multiplying semiconductor device - has well layers multiplying light and barrier layers alternately superposed NoAbstract Dwg 1/1 [Dervent Record]

©Country: JP Japan

%Kind: A (See also: <u>JP2800897B2</u>)

™ Inventor: KAYANE NAOKI;

UOMI KAZUHISA; SAKANO SHINJI;

* Assignee: HITACHI LTD

News, Profiles, Stocks and More about this company

Published / Filed: 1989-06-01 / 1987-11-27

Application Number: JP1987000297550

FIPC Code: H01L 31/14; H01S 3/18;

Spriority Number: 1987-11-27 JP1987000297550 ...

3 Abstract:

PURPOSE: To increase a wavelength band and to decrease an output saturation by forming an active layer of a well layer having small band gap and a barrier layer having large band gap, and setting the thickness of the well layer to de Broglie wavelength or less of electron.

CONSTITUTION: An active layer for amplifying a light is formed in a multiplex quantum well structure, and a plurality of barrier layers 4 each having large band gap and a plurality of well layers each having small band gap are alternately laminated. When the thickness of the layer 3 is reduced to de Broglie wavelength or less of electron, electrons in the well layer are enclosed in a two-dimensional manner. Accordingly, the energy distribution of the electron is narrowed, and a high gain is obtained in a low current concentration. Thus, the operating current density of an optical amplifier can be reduced. Further, when an N-type impurity is implanted to the well layer or the barrier layer, a wavelength band for obtaining a gain is widened, and since the slope of the gain-current characteristic is small, the saturation of the gain is small even with a strong incident light.

COPYRIGHT: (C)1989,JPO&Japio

* INPADOC Legal Status: None Get Now: Family Legal Status Report

Family: Show 2 known family members

♥Other Abstract Info: None